

Information Disclosure Statement
December 12, 2003
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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-5320 621551-4	U.S. SERIAL NO. not yet assigned
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANT(S) <i>10/735-298</i> Cheng-Wen WU, et al.	
	FILING DATE concurrently herewith <i>12/12/03</i>	GROUP not yet assigned <i>2138</i>

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>CB</i> ✓	Chi-Feng Wu, et al., "Simulation-Based Test Algorithm Generation and Port Scheduling for Multi-Port Memories," Proceedings of the 38th Design Automation Conference, DAC 2001, June 18-22, 2001, Las Vegas, NV, USA
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EXAMINER <i>Cynthia Butth</i>	DATE CONSIDERED <i>1-18-06</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.